

## PRIOR ART

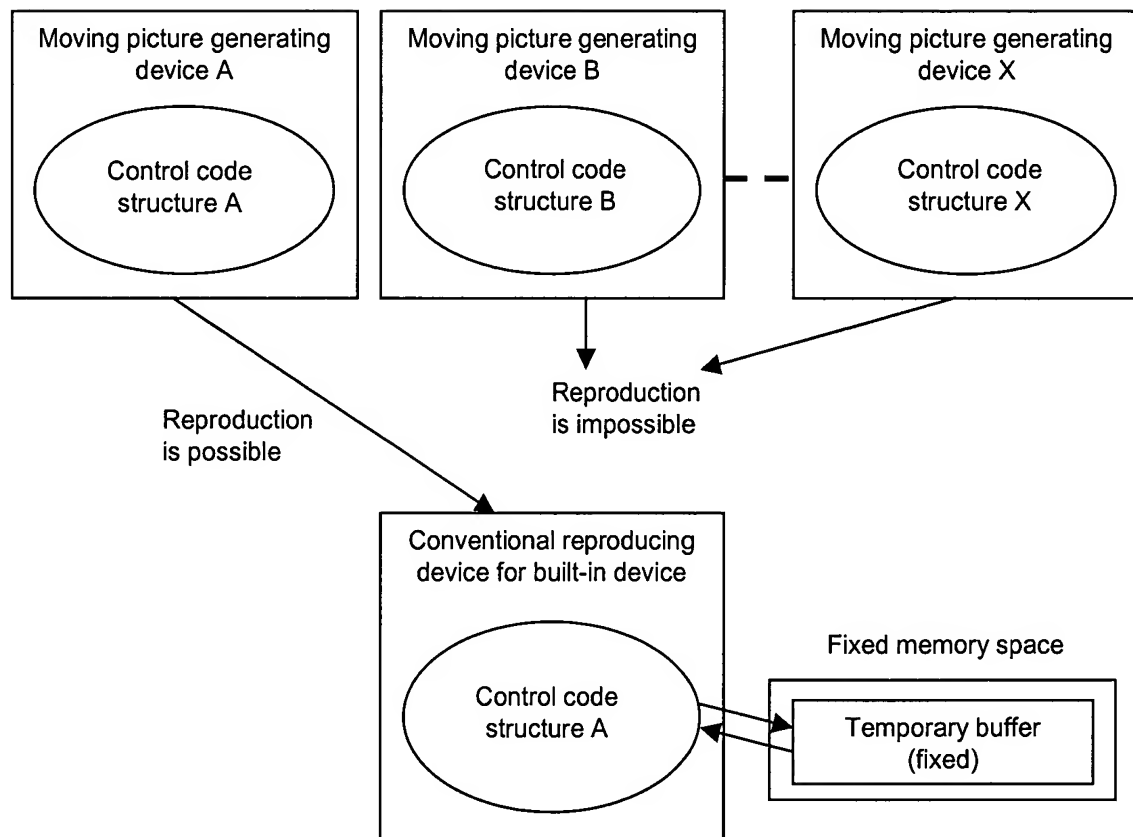


FIG. 1

## PRIOR ART

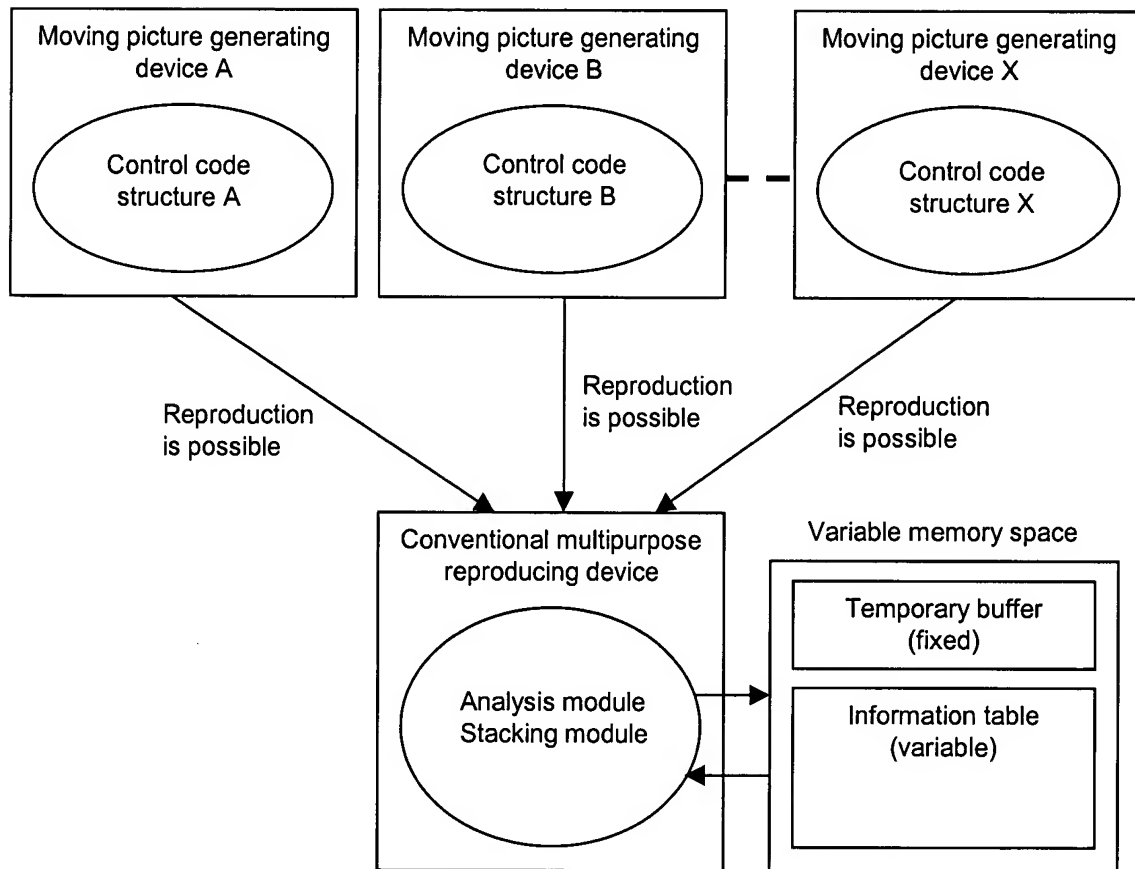


FIG. 2

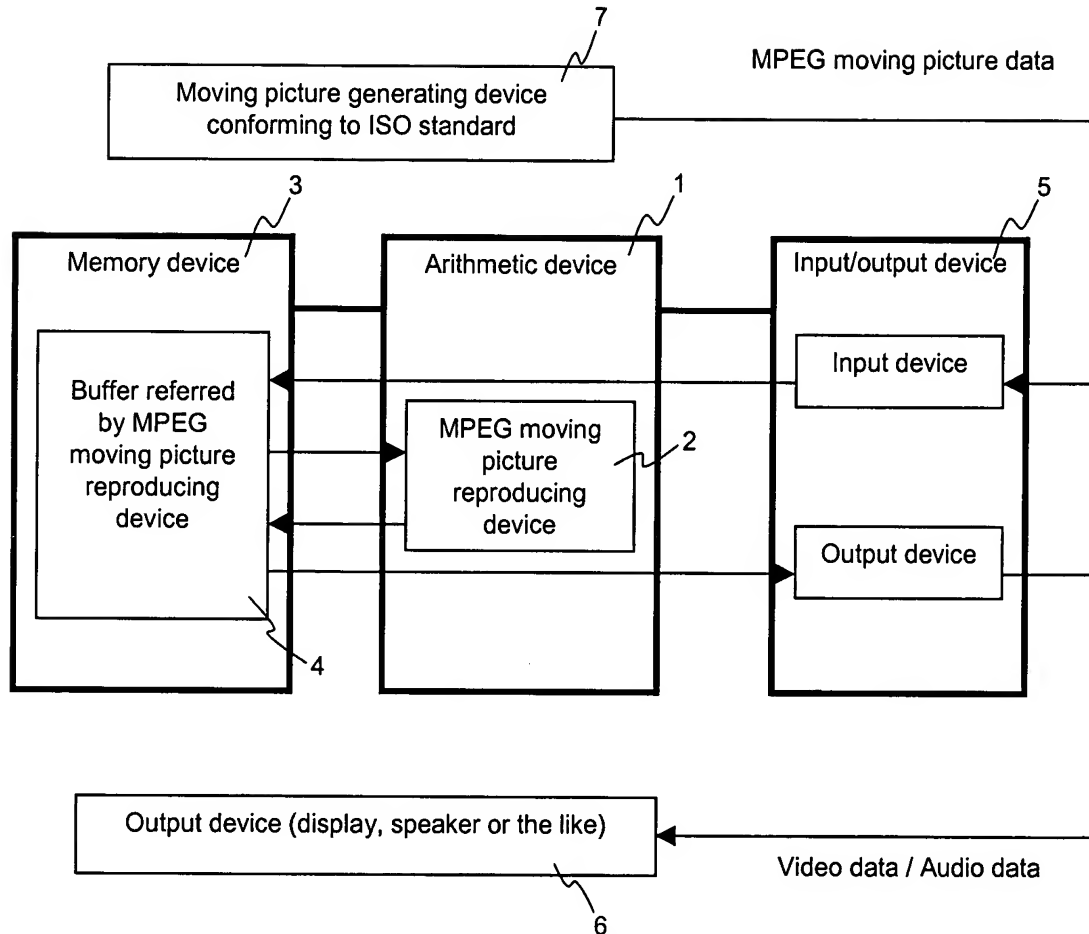


FIG. 3

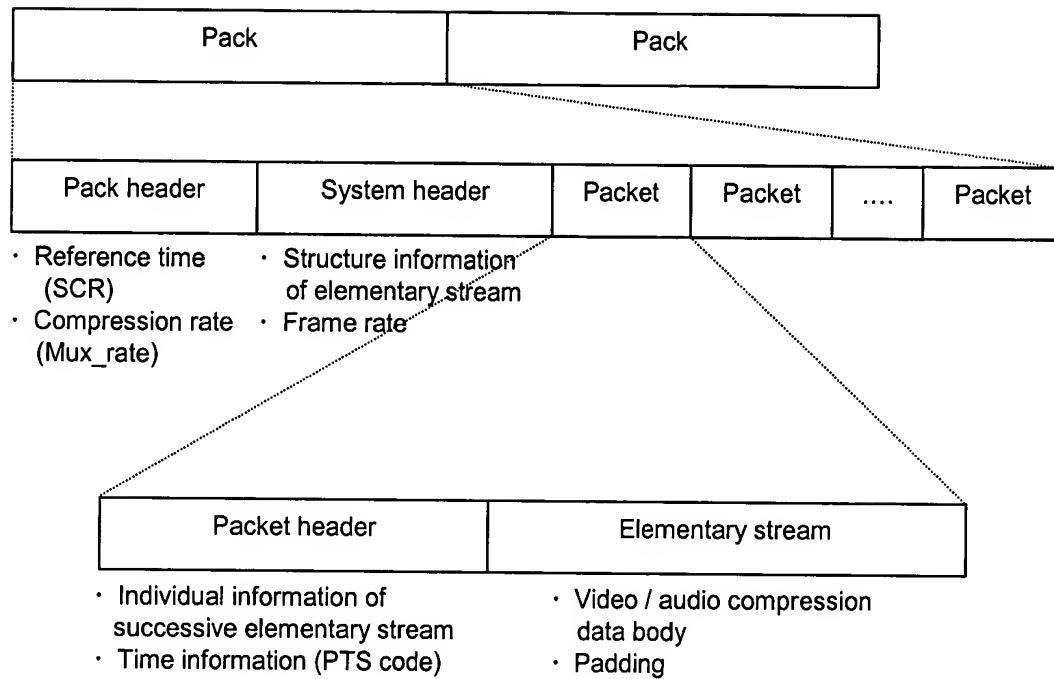


FIG. 4

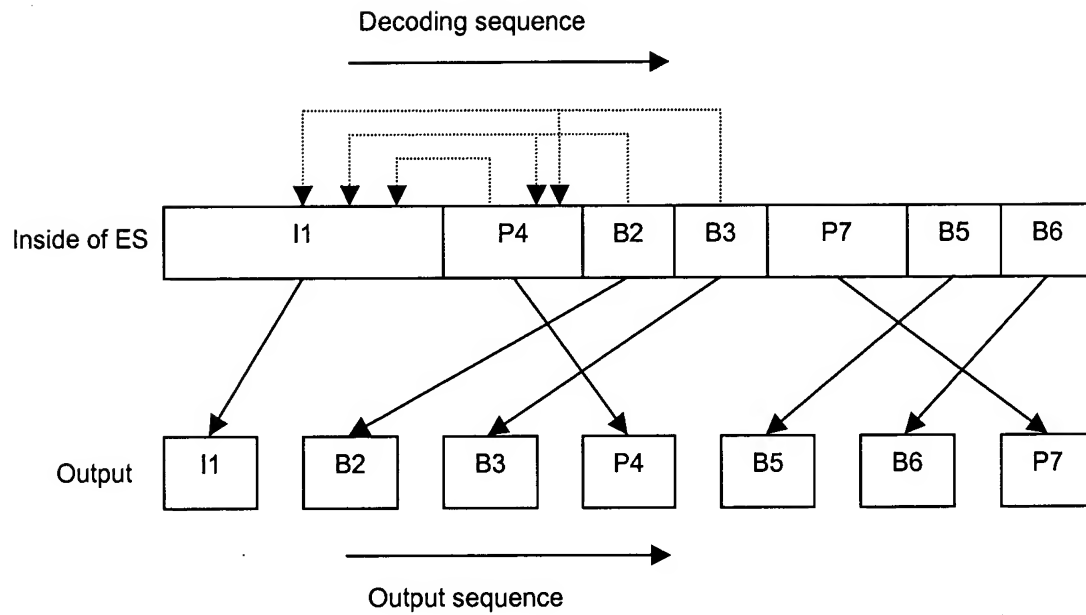
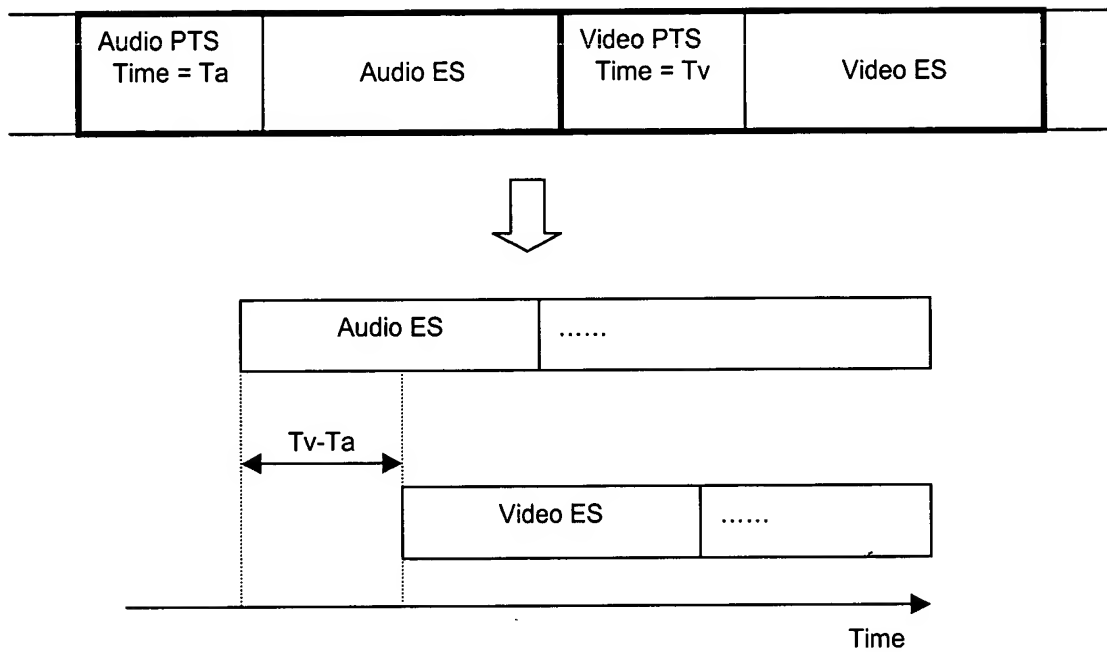


FIG. 5

(1) In the case of  $T_a < T_v$



(2) In the case of  $T_a > T_v$

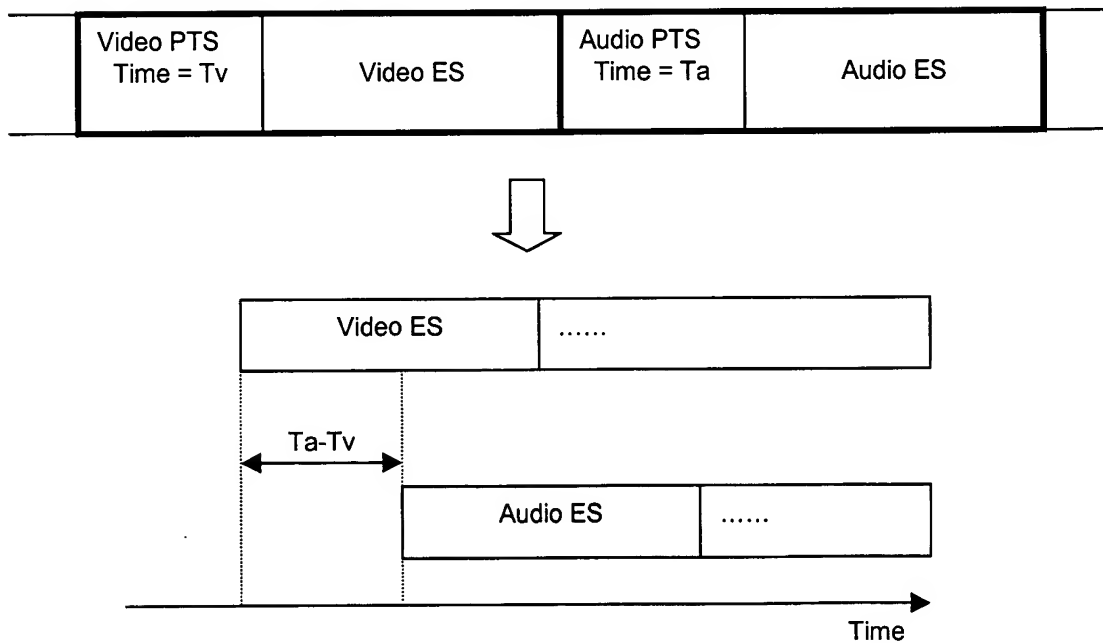


FIG. 6

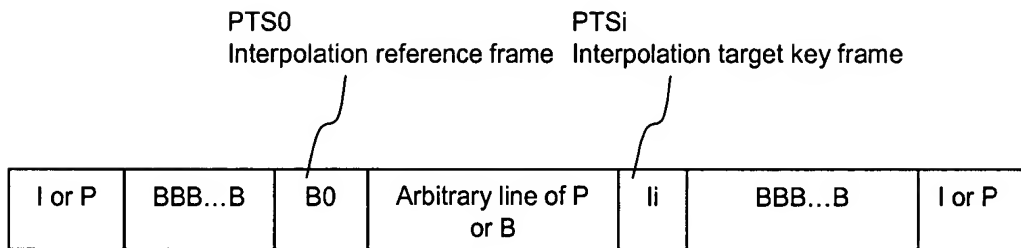


FIG. 7

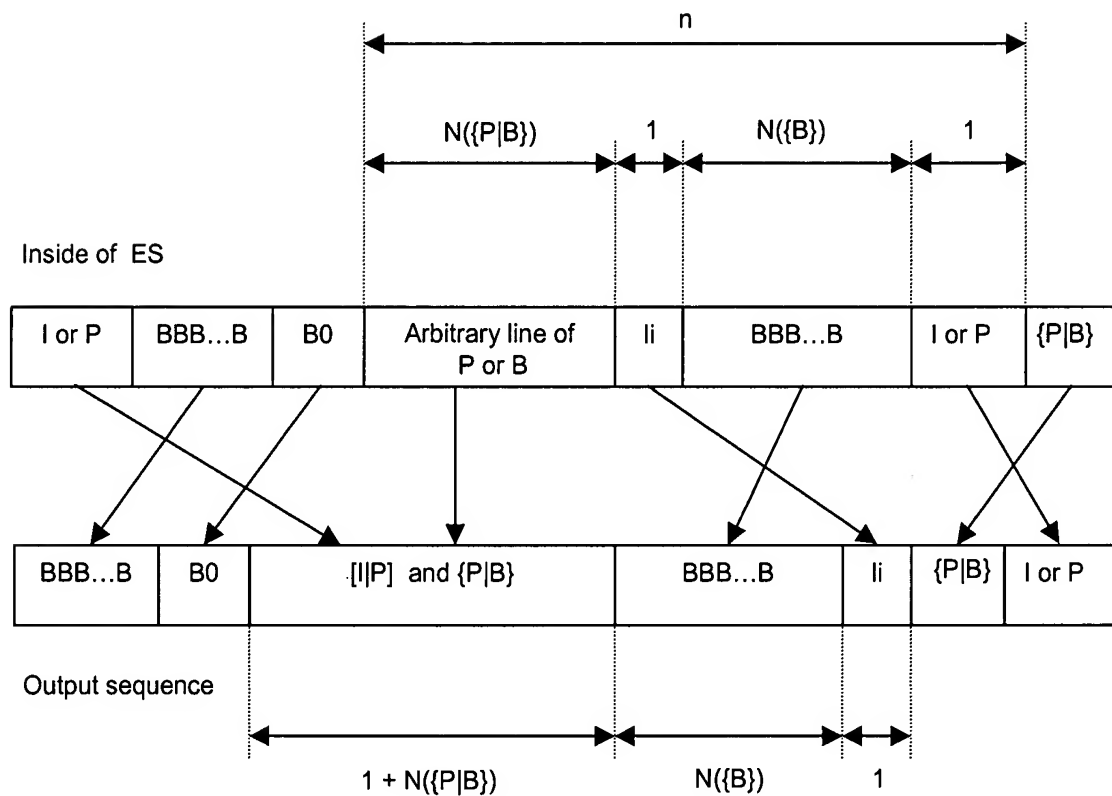


FIG. 8



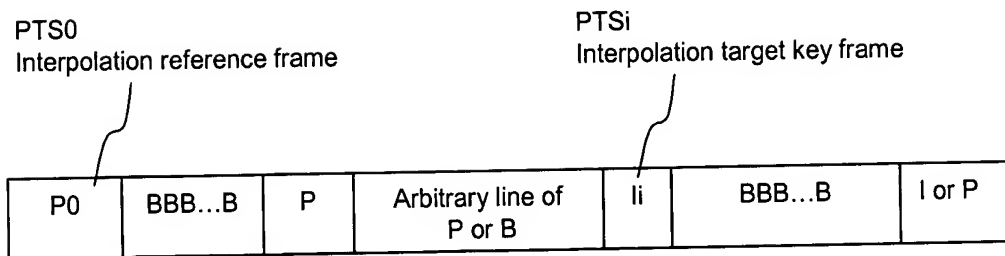


FIG. 9

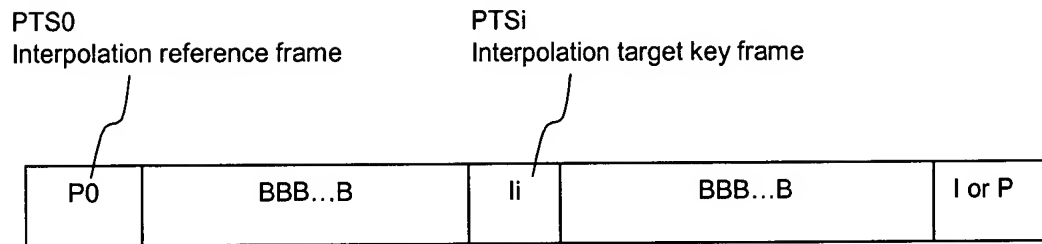


FIG. 10

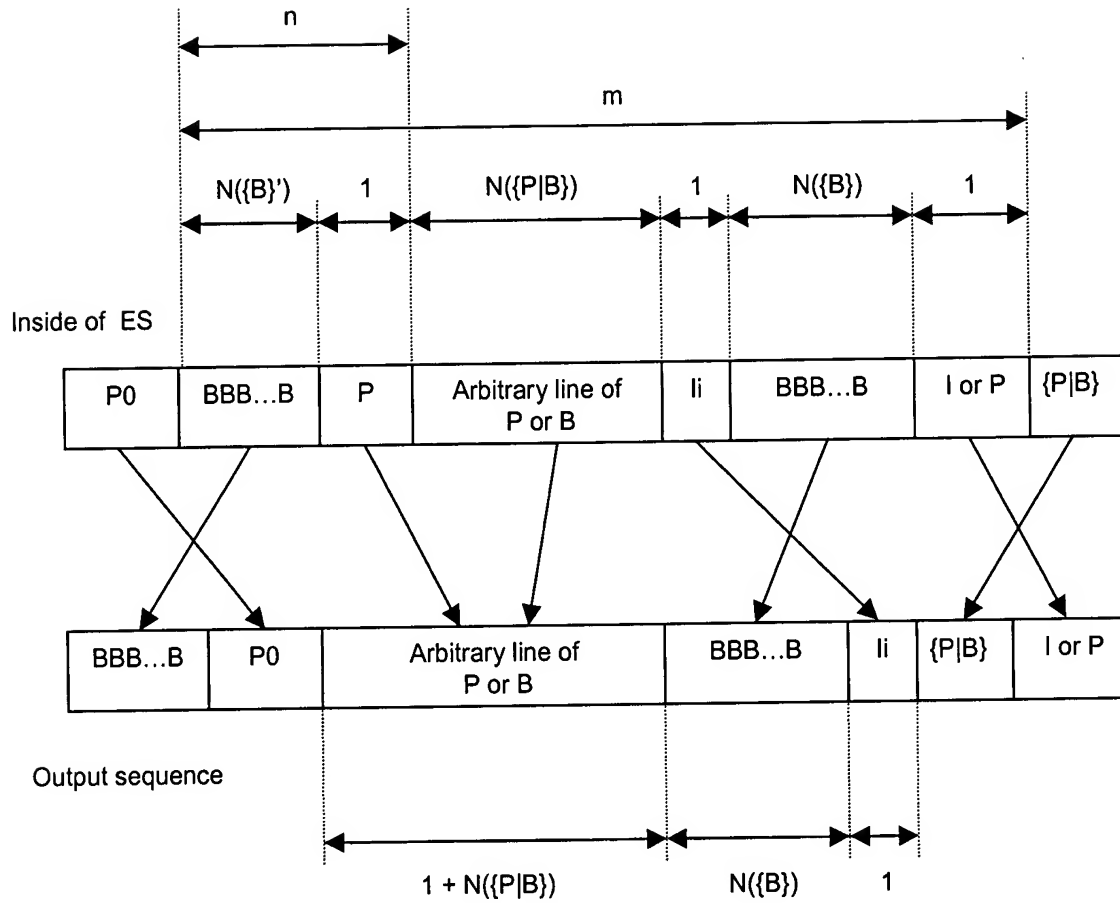


FIG. 11

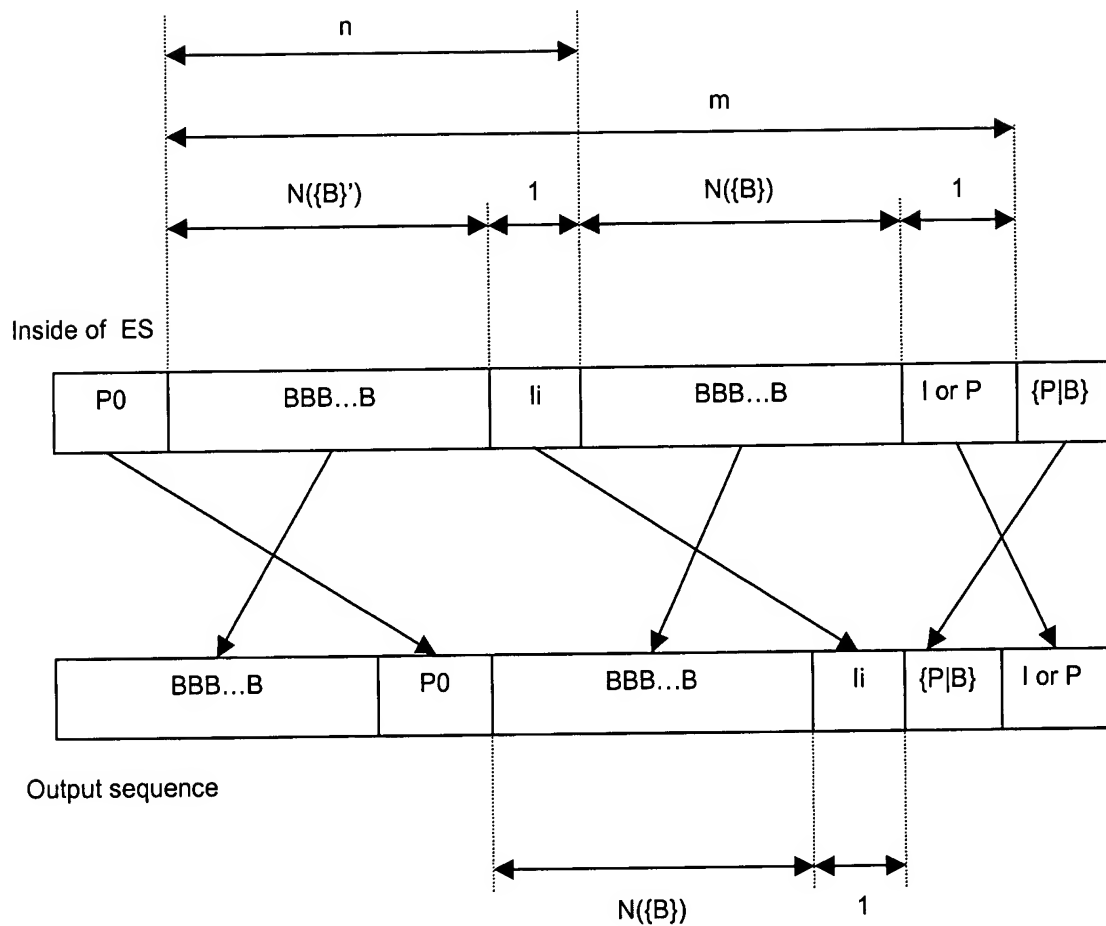


FIG. 12

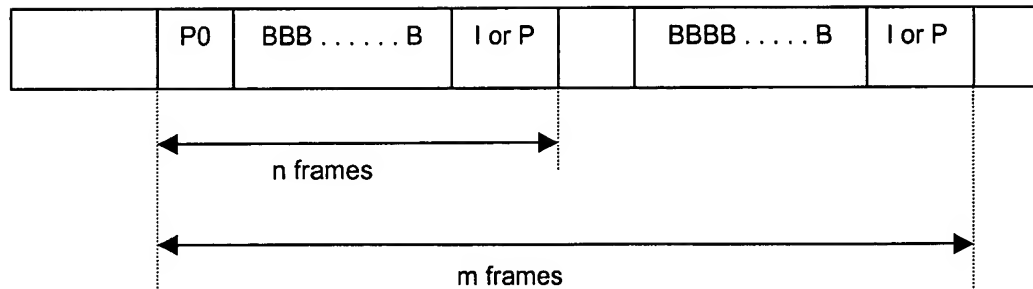


FIG. 13

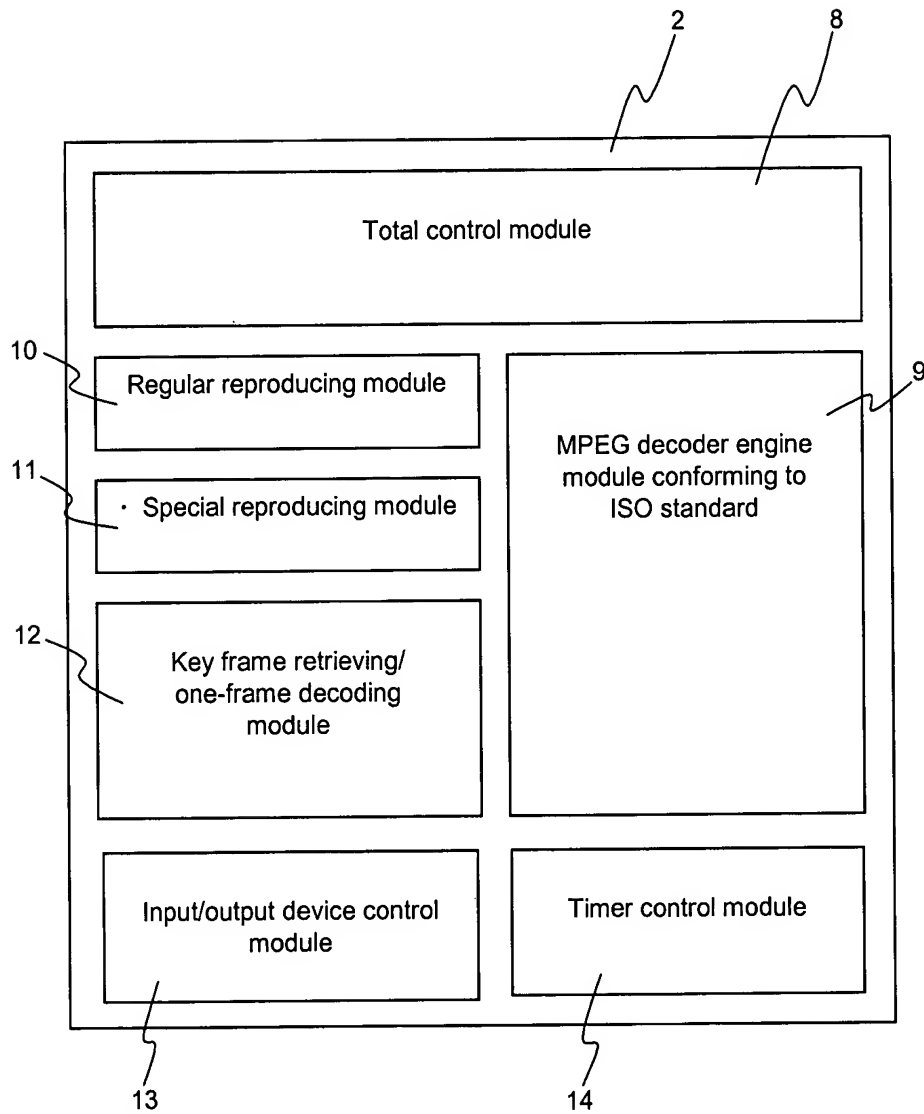


FIG. 14

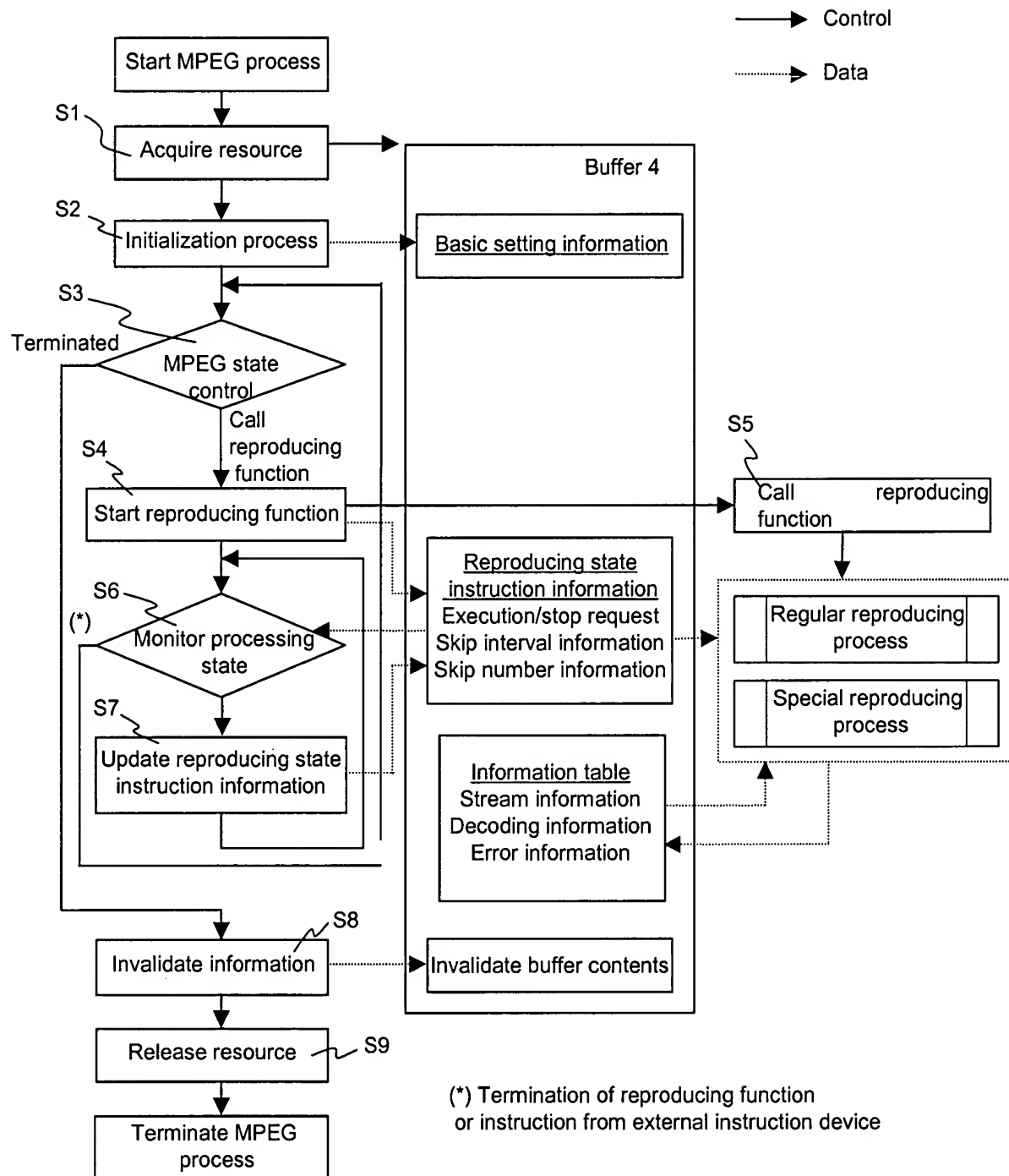


FIG. 15

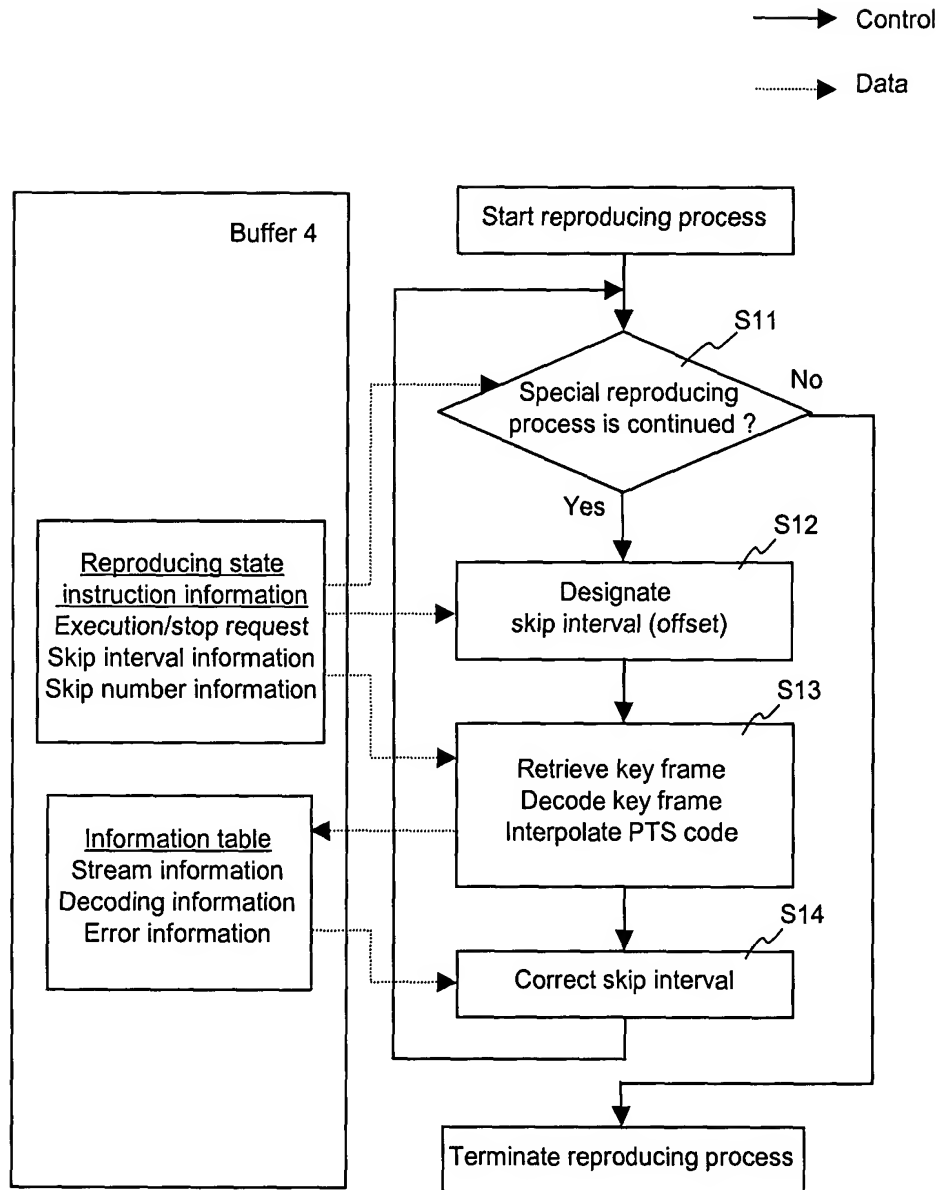


FIG. 16





	MPEG reproducing software of the present invention	Related reproducing device A (application on multipurpose PC)	Related reproducing device B (hardware mounted as ASIC)
Regular reproduction of MPEG moving picture	Corresponding to an optional moving picture data	Corresponding to an optional moving picture data	Corresponding to a particular moving picture data
Special reproduction of MPEG moving picture	Corresponding to an optional moving picture data	Corresponding to an optional moving picture data	Corresponding to a particular moving picture data
Memory used (Data of QVGA is used)	500KByte or less	About 10Mbyte	About 2Mbyte (outside ASIC)
Cycle of arithmetic device used	About 200MHz About 500mW	About 400MHz About 2W	. . .
Merit	High flexibility Resource of CPU and memory is not wasted	High flexibility	Resource of memory is not wasted
Demerit	Interpolation corresponding to all frames and all picture types is impossible	High flexibility is realized by wasting resource of CPU and memory	Low flexibility due to mounted hardware

FIG. 18